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Credit Based VI Semester B.Sc. Degree Examination, September 2022 (2020 – 21 and Earlier Batches) PHYSICS (Paper – VIII) **Electronics**

Time: 3 Hours

Instructions : i) Answer questions from all Units.

- ii) Multiple choice questions must be answered in the first page of the answer book only.
- iii) Scientific calculators are allowed.

PART - A

1. Answer the following questions by choosing the most appropriate answer. $(1 \times 10 = 10)$ i) In a Zener voltage regulator, the Zener diode is ______ biased. a) Forward b) Reverse c) Both forward and reverse d) Unbiased ii) CMRR of an OPAMP is 10⁴. Its value in dB is a) 100 b) 40 c) 80 d) 60 iii) For an OPAMP which of the following is true ? OPAMP amplifies. a) DC signal b) AC signal c) Difference of two inputs d) All [a), b) and c)] iv) A n-channel e-MOSFET can be operated with a) Positive gate voltage only b) Negative gate voltage only c) Both positive and negative gate voltages d) Gate voltage above threshold v) In a feedback amplifier feedback fraction is 1/4. To obtain oscillation gain of the amplifier is a) +3 c) -4 b) -3 d) 4 vi) Oscillators are the amplifiers with input supplied them is c) 1 d) –1 a) 0 b) ∞).

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Max. Marks: 80

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vii)	 OR gate produces output state 1 a) Both the inputs in state 0 b) Both the inputs and either of the c) Either of the inputs state 1 d) Both the inputs in state 1 	when ne inputs s	state 1		
viii)	Serial shift register is the one in wa) One bit at a timec) More than two bits at a time	/hich data b) d)	were entered All the bits at All of the above	same time /e	
ix)	In amplitude modulation, side ban of total power AM transmitted. a) 33.33% b) 50.33%	ids contair c)	ו 100%	of useful power d) 66.66%	
x)	In satellite communication angula cover entire earth (except polar response) 120°	ar separat egion) is	ion between tl	nree satellites to	
2. An i) iii) iv) v) v) vi) vii)	by 120 iswer any five of the following : Draw input and output wave forms What are the values of cut off free Distinguish between BJT and FET Give any two comparisons betwee Draw the logic diagram using NAI Give the truth table of half adder of Draw a block diagram for CRT.	s of a full quency an F. en positive ND gates circuit.	wave rectifier. d band width o e and negative for the equatio	(2×5=1) f IC 741 ? feedbacks. n Y = A + B.	0)
	PA U	nit – I			
3. a)	Construct Zener voltage regulator line regulations.	circuit and	l explain its wo	rking in terms of	4
b)	Explain the concept of virtual grou and obtain expression for voltage OR	nd. Consti gain.	ruct OPAMP in	verting amplifier	6
4. a)	What are the characteristics of ide characteristics of IC 741.	eal OPAM	P? Describe a	any two	4
b)	Explain with a circuit diagram, the obtain expressions for ripple factor	e working o or and effic	of full wave brid ciency.	dge rectifier and	6

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5. a) In a Zener voltage regulator, find line current, load current, Zener current and power dissipated across the load R_L from the data given below : Given : $V_i = 50 \text{ V}, R_s = 5 \text{ K}\Omega, V_z = 10 \text{ V}$ and $R_L = 5 \text{ K}\Omega$.

OR

b) Using OPAMP it is required to design inverting and non-inverting amplifiers. For an input resistance of 1 k Ω and input voltage of 1 V, non-inverting amplifier produces output voltage of 6 V. Determine gain and feedback resistance of non-inverting amplifier. If same combination of resistors were maintained, what would be the gain of the inverting amplifier ?

Unit – II

6.	a)	Using concept of feedback obtain a condition for Barkhausen criterion.	4
	b)	Using suitable diagrams give the construction and working of n-channel e-MOSFET.	6
		OR	
7.	a)	Explain drain and transfer characteristics of d-MOSFET.	4
	b)	What is an oscillator ? With a circuit diagram explain the working of RC phase shift oscillator.	6
8.	a)	The voltage gain of an amplifier with 5% negative feedback is 100. What is the gain without feedback ? Also find the loop gain.	5
		OR	

- b) Using following experimental data of e-MOSFET find :
 - i) $V_{GS(th)}$ voltage
 - ii) AC drain resistance
 - iii) Trans-conductance
 - iv) Amplification factor.

$I_{D (ON)} = 4 \text{ mA}, \text{ k} = 0.278 \text{ mAV}^{-2} \text{ at } V_{GS} = 6.793 \text{ V}.$						
V _{GS} (V)	4	4	5			
V _{DS} (V)	7	12	12			
I _D (mA)	8	8.5	8.25			

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Unit – III

9.	a)	Construct RS	6 flip-flop usin	g NOR gate and explain	its operation.	4	
	b)	Construct an using NAND	d realize the l gate. Comme	logic patterns of NOT, AN ent on the overall result.	ND, OR and XOR gates	6	
			OR				
10.	a)	Using a block decoders.	k diagram exp	plain the working of BCD	to seven segment	4	
	b)	Construct mo explain its wo	od 10-decade orking.	counter. Use timing diag	ram and truth table to	6	
11.	a)	Simplify the f Y = $A\overline{B} + A\overline{B}$	Simplify the following Boolean equation and draw logical diagram for it. Y = $A\overline{B} + A\overline{B}\overline{C} + A\overline{B}\overline{C}\overline{D} + A\overline{B}\overline{C}\overline{D}$				
	 b) Table given below shows output state 1 for following combination of inputs. Using sum of product method set SOP equation, simplify and draw logic diagram for it. 						
		Α	В	Fundamental product			
		1	0	•			
		0	1				
		1	1				

Unit – IV

12.	a)	Describe any two applications of CRO.	4
	b)	Derive an expression for the instantaneous voltage of an AM wave and obtain an expression for total power in terms of Modulation index. OR	6
13.	a)	What is demodulation ? Explain demodulation using diode detector.	4
	b)	Describe the role of ionosphere in sky wave propagation and explain : i) Skip distance	
		ii) Maximum usable frequency.	6
14.	a)	A sinusoidal carrier wave of frequency 10 MHz and amplitude of 60 V is amplitude modulated by 2 KHz wave with modulation index 40%. Find the side band frequencies and their amplitudes. What is the band width of modulated wave ? OR	5
	b)	An AM transmitter radiates radio-wave of 30 kW at modulation index 80%. Calculate the percentage of power associated with carrier wave and each of the side bands.	5